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Docket No.: S&ZIO030801

Thereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandra, VA 22313-1450, on the date indicated below.

Ву: ////

Date: June 7, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No.

10/653,653

Confirmation No: 3425

Applicant Filed

Michael Kandler September 2, 2003

Title

Sensor Module

Art Unit

2855.0

Examiner

Andre J. Allen

Docket No.

S&ZIO030801

Customer No. :

24131

<u>UNDER 37 C.F.R. 1.97(C)(1)</u>

Hon. Commissioner for Patents

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

German Patent DE 198 25 761 C2 (Hillerich et al.), dated December 16, 1999, and English abstract thereof, which discloses an apparatus for detecting the stretching and/or compression of a body. The apparatus comprises a deformable semiconductor substrate 102 on which at least one element having a physical property that changes, depending on the stretching and/or compression of the substrate 102, is arranged (see piezoresistive resistor traces 116 and 118 in Fig. 2 of DE 198 25 761 C2, for example). An evaluation device (see elements 108, 112a, 112b and 110 in Fig. 2) is arranged on the substrate and connected to the elements 116 and 118 to produce a measuring signal depending on the stretching and/or compression of a body to which the apparatus is attached. The evaluation device is arranged on the substrate so that it can be stretched and/or compressed. Switching elements of the evaluation device are arranged so that influences of stretching and/or compression on the function of the evaluation device are minimal.

According to Fig. 3 of DE 198 25 761 C2, the semiconductor substrate 102 is attached to a body 134 by an adhesive layer 136, for example. In addition, a foil 120 having an opening 122 is provided for protection. It is indicated in column 7, lines 38-41 of DE 198 25 761 C2 that, likewise, a foil 120 without an opening could be used. According to Fig. 4A of DE 198 25 761 C2, a foil 120 without an opening is used. Moreover, according to Fig. 4A, the conformable semiconductor substrate 102 is attached to a lower support foil 138.

Finally, it is outlined in column 10, lines 24 to 31 that the evaluation device may include a wireless RF transceiver, a so-called transponder to integrate such a transponder with the evaluation device. In such a case, the required antenna can be integrated within the upper or lower support foil, for example.

Fig. 5 of DE 198 25 761 C2 shows a block diagram of an evaluation means comprising a memory 538.

German Examination Report dated May 2, 2005.

In accordance with 37 C.F.R. 1.97(e) the undersigned herewith states that each item of information contained in the information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement.

In accordance with 37 C.F.R. 1.97 (c) (1), consideration of this Information Disclosure Statement is requested.

Respectfully submitted.

52,794

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Date: June 7, 2005

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